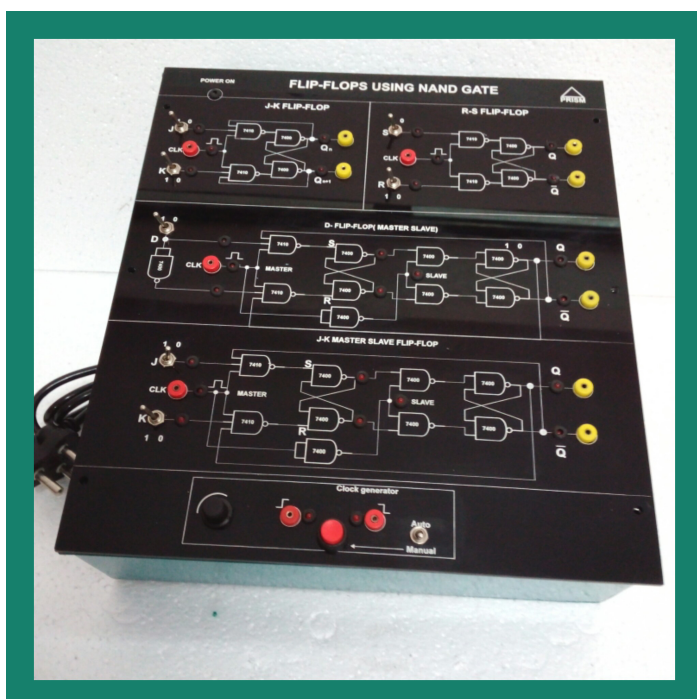




ELECTRONICS SYSTEMS
(Division of Electronics Instrumentation & Lab Equipments)

STUDY OF FLIP-FLOP USING NAND GATE



Application areas : Schools & Science college (Physics , Electronics & Computer) , Polytechnic & Engineering laboratories, Research purpose , Universities.

Technical specifications:

1. 230 V AC , 50Hz operated table top model.
2. Experimental circuits are engraved on quality polycarbonate acrylic sheet to understand the circuits clearly.
3. All major 3mm banana test points are located on the kit wherever necessary.
4. Inbuilt variable (0-12 v dc) power supply.
5. Inbuilt Clock generator +ve & -ve edge triggered.
6. Auto /manual clock.
7. Kit is fitted in MS box with water-green powder coating finish.
8. Above trainer kits are manufactured using quality components.
9. Size: 330 x 220 x 90 mm (L X B X H) , 0.8 thickness.
10. Supplied with experimental details manual & connecting molded pins patch cords.

Experiments can be done using kit as,

1. To study the flip-flops using NAND gates.

